

ISCA 2025 Industrial Session Call for Papers

Submission Guidelines

- The papers ideally include (1) retrospective evaluations of real working products, (2) upcoming industry products on their roadmaps, and/or (3) planned products that were canceled but present interesting insights or lessons learned.
- The following submissions will not be considered: (1) short-term internship projects in industries, or (2) speculation about hardware that might be built.
- The first and most of the authors of such papers must work in industry.
- The submissions are required to disclose the affiliations of all authors. Reviewers want to know which product is being evaluated and which company is writing the paper. The review process will follow common practice to avoid conflicts of interest.
- All submissions to the industry track must follow general submission guidelines (including font and page limits). **Submissions that fail to abide by the guidelines will be rejected without review.**
- *Select submission may be considered for an upcoming IEEE Micro Special Issue.*

Important Dates

- Abstract due: **January 3, 2025 at 11:59 PM EST**
- Full Paper due: **January 13, 2025 at 11:59 PM EST**
- Reviews: **March 3, 2025 (to authors)**
- Rebuttal due: **March 7, 2025**
- Online PC: **March 17, 2025**
- Final Reviews: **March 21, 2025**
- Conference: **June 21 – 25, 2025**

Note: **1)** The program committee recognizes that industry papers need to be approved by management (often involving multiple rounds of redaction) before they can be submitted, and there can be restrictions about filing patents before submitting a paper. Therefore, a later deadline is adopted to increase the chances of receiving such papers. **2)** The 2025 ISCA industrial session will include a lighter weight rebuttal process to maximize industry submissions as well industry involvement in the review process.

Topics of Interest

Paper topics are not limited to hardware tapeouts. Papers that are software-centric papers relevant to the ISCA audience are welcome in this track (e.g. datacenter software work, compiler work, accelerator software stack work), but they should adhere to the tenet that they must be industry papers about production-level work - whether retrospective, planned and on the roadmap, or planned but canceled.

- Processors, SoCs, GPUs, and domain-specific accelerators
- Systems and interconnect technologies for HPC, cloud, or data centers
- Embedded, mobile, and IoT processors
- FPGA or reconfigurable architectures
- Storage and emerging memory systems
- Architectures using emerging technology.
- Architectures for emerging applications including generative AI and bioinformatics.
- Architectures for commercialization of quantum computing

